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Vishay Siliconix

Automotive N-Channel 40 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	40				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.0056				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 \text{ V}$	0.0070				
I _D (A)	50				
Configuration	Single				
Package	TO-252				



FEATURES

- TrenchFET® power MOSFET
- Package with low thermal resistance
- AEC-Q101 qualified
- 100 % R_a and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



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N-Channel MOSFET	o s

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V _{DS}	40	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current	T _C = 25 °C ^a		50	
Continuous Drain Current	T _C = 125 °C	I _D	45	
Continuous Source Current (Diode Conduction) ^a		Is	50	Α
Pulsed Drain Current ^b		I _{DM}	200	
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	33	
Single Pulse Avalanche Energy	L=0.1 IIIA	E _{AS}	54	mJ
Maximum Power Dissipation ^b	T _C = 25 °C	D	71	W
	T _C = 125 °C	P_D	23	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient Po	CB Mount c	R _{thJA}	50	°C/W	
Junction-to-Case (Drain)		R _{thJC}	2.1	C/VV	

Notes

- a. Package limited.
- b. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- c. When mounted on 1" square PCB (FR4 material).

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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static								
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		40	-	-	V	
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		2.0	2.5	V	
Gate-Source Leakage	I _{GSS}	V _{DS} =	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	± 100	nA	
		V _{GS} = 0 V	V _{DS} = 40 V	=	-	1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = 40 V, T _J = 125 °C	=	-	50	μΑ	
		$V_{GS} = 0 V$	V _{DS} = 40 V, T _J = 175 °C	=	-	150	•	
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 V$	50	-	-	Α	
		V _{GS} = 10 V	I _D = 20 A	-	0.0043	0.0056		
Drain-Source On-State Resistance a	В	V _{GS} = 10 V	I _D = 20 A, T _J = 125 °C	=	-	0.0094		
Drain-Source On-State Resistance ~	R _{DS(on)}	V _{GS} = 10 V	I _D = 20 A, T _J = 175 °C	=.	-	0.0115	Ω	
		V _{GS} = 4.5 V	I _D = 15 A	=	0.0057	0.0070		
Forward Transconductance b	9fs	V _{DS} = 15 V, I _D = 15 A		=.	100	-	S	
Dynamic ^b								
Input Capacitance	C _{iss}			1	2850	4000		
Output Capacitance	C _{oss}	$V_{GS} = 0 V$	V _{DS} = 25 V, f = 1 MHz	-	360	500	pF	
Reverse Transfer Capacitance	C _{rss}			=	135	200		
Total Gate Charge ^c	Qg			1	46	75		
Gate-Source Charge c	Q_{gs}	V _{GS} = 10 V	$V_{DS} = 20 \text{ V}, I_{D} = 50 \text{ A}$	-	10	-	nC	
Gate-Drain Charge ^c	Q _{gd}			-	8	-		
Gate Resistance	R_g	f = 1 MHz		1.3	2.8	4.5	Ω	
Turn-On Delay Time ^c	t _{d(on)}			=	9	15		
Rise Time ^c	t _r	$V_{DD} = 20 \text{ V}, R_L = 0.4 \Omega$ $I_D \cong 50 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		=	19	30	ns	
Turn-Off Delay Time ^c	t _{d(off)}			-	26	40		
Fall Time ^c	t _f			=	10	15		
Source-Drain Diode Ratings and Chara	cteristics ^b							
Pulsed Current ^a	I _{SM}			-	-	200	Α	
Forward Voltage	V _{SD}	I _F = 30 A, V _{GS} = 0 V		-	0.87	1.5	V	

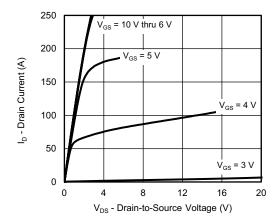
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

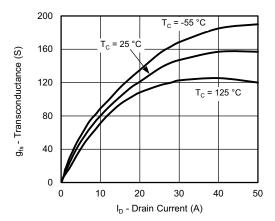
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



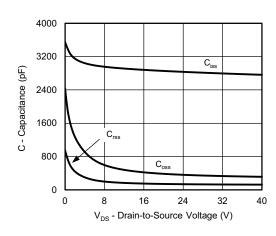
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



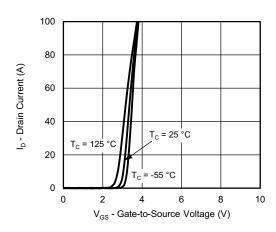
Output Characteristics



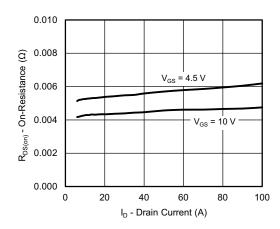
Transconductance



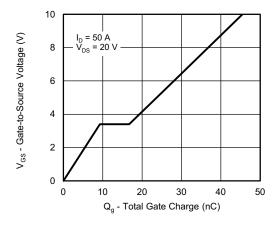
Capacitance



Transfer Characteristics



On-Resistance vs. Drain Current

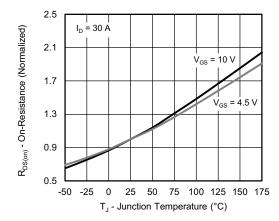


Gate Charge

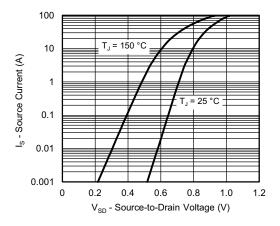
For technical questions, contact: automostechsupport@vis



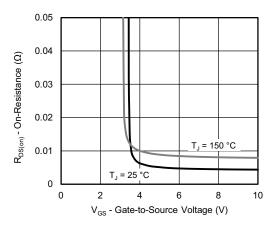
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



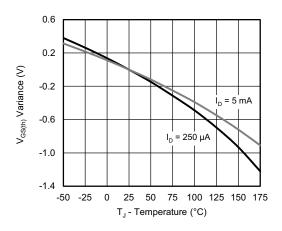
On-Resistance vs. Junction Temperature



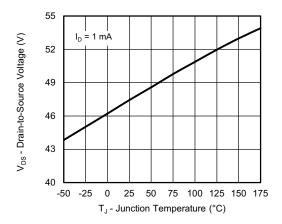
Source Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

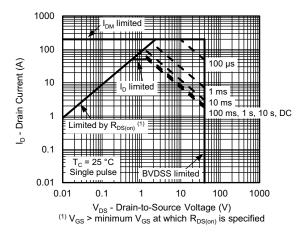


Drain Source Breakdown vs. Junction Temperature

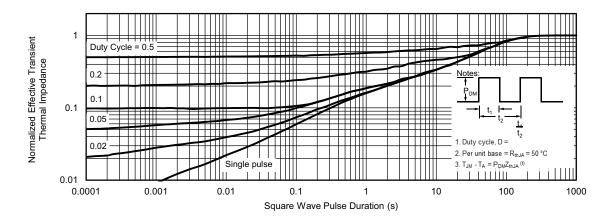
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THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



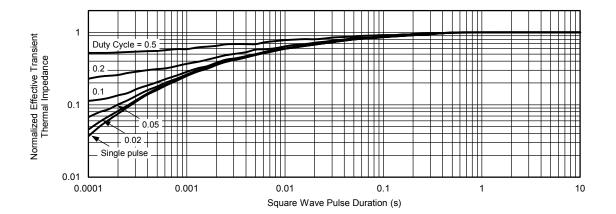
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction to Case (25 °C)

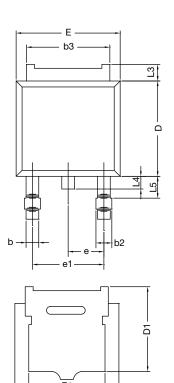
are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

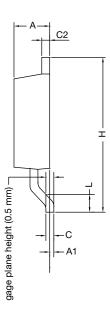
Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?63413.



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TO-252AA Case Outline





	MILLIN	METERS	INC	HES	
DIM.	MIN.	MAX.	MIN.	MAX.	
Α	2.18	2.38	0.086	0.094	
A1	-	0.127	-	0.005	
b	0.64	0.88	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	
С	0.46	0.61	0.018	0.024	
C2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	
D1	4.10	-	0.161	-	
Е	6.35	6.73	0.250	0.265	
E1	4.32	-	0.170	-	
Н	9.40	10.41	0.370	0.410	
е	2.28	BSC	0.090	BSC	
e1	4.56 BSC		0.180 BSC		
L	1.40	1.78	0.055	0.070	
L3	0.89	1.27	0.035	0.050	
L4	-	1.02	-	0.040	
L5	1.01	1.52	0.040	0.060	
ECN: T13-0592-Rev. A, 02-Sep-13					

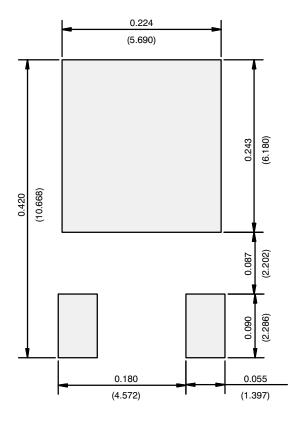
DWG: 6019

Note

• Dimension L3 is for reference only.



RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index

APPLICATION NOTE



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